

SqueezeDet: Unified, Small, Low Power Fully Convolutional Neural Networks for Real-Time Object Detection for Autonomous Driving

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Abstract

Object detection is a crucial task for autonomous driving. In addition to requiring high accuracy to ensure safety, object detection for autonomous driving also requires real-time inference speed to guarantee prompt vehicle control, as well as small model size and energy efficiency to enable embedded system deployment.

In this work, we propose SqueezeDet, a fully convolutional neural network for object detection that aims to simultaneously satisfy all of the above constraints. In our network we use convolutional layers not only to extract feature maps, but also as the output layer to compute bounding boxes and class probabilities. The detection pipeline of our model only contains a single forward pass of a neural network, thus it is extremely fast. Our model is fully-convolutional, which leads to small model size and better energy efficiency. Finally, our experiments show that our model is very accurate, achieving state-of-the-art accuracy on the KITTI [9] benchmark. The source code of SqueezeDet is open-source released¹.

1. Introduction

A safe and robust autonomous driving system relies on accurate perception of the environment. To be more specific, an autonomous vehicle needs to accurately detect cars, pedestrians, cyclists, road signs, and other objects in real-time in order to make the right control decisions that ensure safety. Moreover, to be economical and widely deployable, this object detector must operate on embedded processors that dissipate far less power than powerful GPUs used for benchmarking in typical computer vision experiments.

Object detection is a crucial task for autonomous driving. Different autonomous vehicle solutions may have different combinations of perception sensors, but image based object detection is almost irreplaceable. Image sensors are cheap compared with others such as LIDAR. Image data (including video) are much more abundant than, for example, LIDAR cloud points, and are much easier to collect and annotate. Recent progress in deep learning shows a promising trend that with more and more data that cover all kinds

of long-tail scenarios, we can always design more powerful neural networks with more parameters to digest the data and become more accurate and robust.

While recent research has been primarily focused on improving accuracy, for actual deployment in an autonomous vehicle, there are other issues of image object detection that are equally critical. For autonomous driving some basic requirements for image object detectors include the following: a) Accuracy. More specifically, the detector ideally should achieve 100% recall with high precision on objects of interest. b) Speed. The detector should have real-time or faster inference speed to reduce the latency of the vehicle control loop. c) Small model size. As discussed in [16], smaller model size brings benefits of more efficient distributed training, less communication overhead to export new models to clients through wireless update, less energy consumption and more feasible embedded system deployment. d) Energy efficiency. Desktop and rack systems may have the luxury of burning 250W of power for neural network computation, but embedded processors targeting automotive market must fit within a much smaller power and energy envelope. While precise figures vary, the new Xavier² processor from Nvidia, for example, is targeting a 20W thermal design point. Processors targeting mobile applications have an even smaller power budget and must fit in the 3W–10W range. Without addressing the problems of a) accuracy, b) speed, c) small model size, and d) energy efficiency, we won't be able to truly leverage the power of deep neural networks for autonomous driving.

In this paper, we address the above issues by presenting *SqueezeDet*, a fully convolutional neural network for object detection. The detection pipeline of SqueezeDet is inspired by [21]: first, we use stacked convolution filters to extract a high dimensional, low resolution feature map for the input image. Then, we use *ConvDet*, a convolutional layer to take the feature map as input and compute a large amount of object bounding boxes and predict their categories. Finally, we filter these bounding boxes to obtain final detections. The “backbone” convolutional neural net (CNN) architecture of our network is SqueezeNet [16], which achieves AlexNet level imageNet accuracy with a model size of < 5MB that

¹<https://github.com/BichenWuUCB/squeezeDet>

²<https://blogs.nvidia.com/blog/2016/09/28/xavier/>

can be further compressed to 0.5MB. After strengthening the SqueezeNet model with additional layers followed by *ConvDet*, the total model size is still less than 8MB. The inference speed of our model can reach 57.2 FPS³ with input image resolution of 1242x375. Benefiting from the small model size and activation size, SqueezeDet has a much smaller memory footprint and requires fewer DRAM accesses, thus it consumes only 1.4J of energy per image on a TITAN X GPU, which is about 84X less than a Faster R-CNN model described in [2]. SqueezeDet is also very accurate. One of our trained SqueezeDet models achieved the best average precision in all three difficulty levels of cyclist detection in the KITTI object detection challenge [9].

The rest of the paper is organized as follows. We first review related work in section 2. Then, we introduce our detection pipeline, the *ConvDet* layer, the training protocol and network design of SqueezeDet in section 3. In section 4, we report our experiments on the KITTI dataset, and discuss accuracy, speed, parameter size of our model. Due to limited page length, we put energy efficiency discussion in the supplementary material to this paper. We conclude the paper in section 5.

2. Related Work

2.1. CNNs for object detection

From 2005 to 2013, various techniques were applied to advance the accuracy of object detection on datasets such as PASCAL [7]. In most of these years, versions of HOG+SVM [5] or DPM [8] led the state-of-art accuracy on these datasets. However, in 2013, Girshick *et al.* proposed Region-based Convolutional Neural Networks (R-CNN) [11], which led to substantial gains in object detection accuracy. The R-CNN approach begins by identifying region proposals (i.e. regions of interest that are likely to contain objects) and then classifying these regions using a CNN. One disadvantage of R-CNN is that it computes the CNN independently on each region proposal, leading to time-consuming (≤ 1 fps) and energy-inefficient (≥ 200 J/frame) computation. To remedy this, Girshick *et al.* experimented with a number of strategies to amortize computation across the region proposals [13, 17, 10], culminating in *Faster R-CNN* [22]. An other model, R-FCN, is fully-convolutional and delivers accuracy that is competitive with R-CNN, but R-FCN is fully-convolutional which allows it to amortize more computation across the region proposals.

There have been a number of works that have adapted the R-CNN approach to address object detection for autonomous driving. Almost all the top-ranked published methods on the KITTI leader board are based on Faster R-CNN. [2] modified the CNN architecture to use shallower networks to improve accuracy. [3, 26] on the other hand

³Standard camera frame rate is 30 FPS, which is regarded as the benchmark of the real-time speed.

focused on generating better region proposals. Most of these methods focused on better accuracy, but to our knowledge, no previous methods have reported real-time inference speeds on KITTI dataset.

Region proposals are a cornerstone in all of the object detection methods that we have discussed so far. However, in YOLO (You Only Look Once) [21], region proposition and classification are integrated into one single stage. Compared with R-CNN and Faster R-CNN based methods, YOLO’s single stage detection pipeline is extremely fast, making YOLO the first CNN based general-purpose object detection model that achieved real-time speed.

2.2. Small CNN models

For any particular accuracy level on a computer vision benchmark, it is usually feasible to develop multiple CNN architectures that are able to achieve that level of accuracy. Given the same level of accuracy, it is often beneficial to develop smaller CNNs (i.e. CNNs with fewer model parameters), as discussed in [16]. AlexNet [18] and VGG-19 [23] are CNN model architectures that were designed for image classification and have since been modified to address other computer vision tasks. The AlexNet model contains 240MB of parameters, and it delivers approximately 80% top-5 accuracy on ImageNet [6] image classification. The VGG-19 model contains 575MB of parameters and delivers $\sim 87\%$ top-5 accuracy on ImageNet. However, models with fewer parameters can deliver similar levels of accuracy. The SqueezeNet [16] model has only 4.8MB of parameters (50x smaller than AlexNet), and it matches or exceeds AlexNet-level accuracy on ImageNet. The GoogLeNet-v1 [25] model only has 53MB of parameters, and it matches VGG-19-level accuracy on ImageNet.

2.3. Fully convolutional networks

Fully-convolutional networks (FCN) were popularized by Long *et al.*, who applied them to the semantic segmentation domain [20]. FCN defines a broad class of CNNs, where the output of the final parameterized layer is a grid rather than a vector.⁴ This is useful in semantic segmentation, where each location in the grid corresponds to the predicted class of a pixel.

FCN models have been applied in other areas as well. To address the image classification problem, a CNN needs to output a 1-dimensional vector of class probabilities. One common approach is to have one or more *fully-connected layers*, which by definition output a 1D vector $- 1 \times 1 \times \text{Channels}$ (e.g. [18, 23]). However, an alternative approach is to have the final parameterized layer be a convolutional layer that outputs a grid ($H \times W \times \text{Channels}$),

⁴By “parameterized layer,” we are referring to layers (e.g. convolution or fully-connected) that have parameters that are learned from data. Pooling or ReLU layers are not parameterized layers because they have no learned parameters.

and to then use average-pooling to downsample the grid to $1 \times 1 \times \text{Channels}$ to a vector of produce class probabilities (e.g. [16, 19]). Finally, the R-FCN method that we mentioned earlier in this section is a fully-convolutional network.

3. Method Description

3.1. Detection Pipeline

Inspired by YOLO [21], we also adopt a single-stage detection pipeline: region proposition and classification is performed by one single network simultaneously. As shown in Fig. 1, a convolutional neural network first takes an image as input and extract a low-resolution, high dimensional feature map from the image. Then, the feature map is fed into the *ConvDet* layer to compute bounding boxes centered around $W \times H$ uniformly distributed spatial grids. Here, W and H are number of grid centers along horizontal and vertical axes.

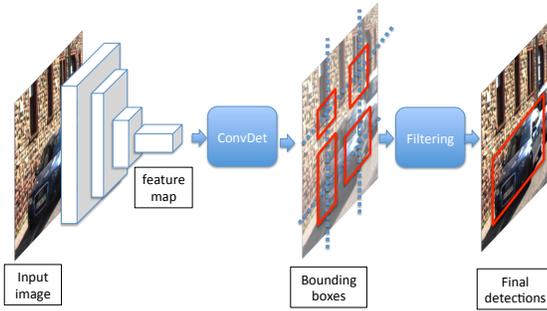


Figure 1. SqueezeDet detection pipeline. A convolutional neural network extracts a feature map from the input image and feeds it into the *ConvDet* layer. The *ConvDet* layer then computes bounding boxes centered around $W \times H$ uniformly distributed grid centers. Each bounding box is associated with 1 confidence score and C conditional class probabilities. Then, we keep the top N bounding boxes with highest confidence and use NMS to filter them to get the final detections.

Each bounding box is associated with $C + 1$ values, where C is the number of classes to distinguish, and the extra 1 is for the confidence score, which indicates how likely does the bounding box actually contain an object. Similarly to YOLO [21], we define the confidence score as $Pr(\text{Object}) * IOU_{truth}^{pred}$. A high confidence score implies a high probability that an object of interest does exist and that the overlap between the predicted bounding box and the ground truth is high. The other C scalars represents the conditional class probability distribution given that the object exists within the bounding box. More formally, we denote the conditional probabilities as $Pr(\text{class}_c | \text{Object})$, $c \in$

$[1, C]$. We assign the label with the highest conditional probability to this bounding box and we use

$$\max_c Pr(\text{class}_c | \text{Object}) * Pr(\text{Object}) * IOU_{truth}^{pred}$$

as the metric to estimate the confidence of the bounding box prediction.

Finally, we keep the top N bounding boxes with the highest confidence and use Non-Maximum Suppression (NMS) to filter redundant bounding boxes to obtain the final detections. During inference, the entire detection pipeline consists of only one forward pass of one neural network with minimal post-processing.

3.2. ConvDet

The *SqueezeDet* detection pipeline is inspired by YOLO [21]. But as we will describe in this section, the design of the *ConvDet* layer enables SqueezeDet to generate tens-of-thousands of region proposals with much fewer model parameters compared to YOLO.

ConvDet is essentially a convolutional layer that is trained to output bounding box coordinates and class probabilities. It works as a sliding window that moves through each spatial position on the feature map. At each position, it computes $K \times (4 + 1 + C)$ values that encode the bounding box predictions. Here, K is the number of reference bounding boxes with pre-selected shapes. Using the notation from [22], we call these reference bounding boxes as anchor. Each position on the feature map corresponds to a grid center in the original image, so each anchor can be described by 4 scalars as $(\hat{x}_i, \hat{y}_j, \hat{w}_k, \hat{h}_k)$, $i \in [1, W]$, $j \in [1, H]$, $k \in [1, K]$. Here \hat{x}_i, \hat{y}_j are spatial coordinates of the reference grid center (i, j) . \hat{w}_k, \hat{h}_k are the width and height of the k -th reference bounding box. We used the method described by [2] to select reference bounding box shapes to match the data distribution.

For each anchor (i, j, k) , we compute 4 relative coordinates $(\delta x_{ijk}, \delta y_{ijk}, \delta w_{ijk}, \delta h_{ijk})$ to transform the anchor into a predicted bounding box, as shown in Fig. 2. Following [12], the transformation is described by

$$\begin{aligned} x_i^p &= \hat{x}_i + \hat{w}_k \delta x_{ijk}, \\ y_j^p &= \hat{y}_j + \hat{h}_k \delta y_{ijk}, \\ w_k^p &= \hat{w}_k \exp(\delta w_{ijk}), \\ h_k^p &= \hat{h}_k \exp(\delta h_{ijk}), \end{aligned} \tag{1}$$

where $x_i^p, y_j^p, w_k^p, h_k^p$ are predicted bounding box coordinates. As explained in the previous section, the other $C + 1$ outputs for each anchor encode the confidence score for this prediction and conditional class probabilities.

ConvDet is similar to the last layer of RPN in Faster R-CNN [22]. The major difference is that, RPN is regarded as a “weak” detector that is only responsible for detecting

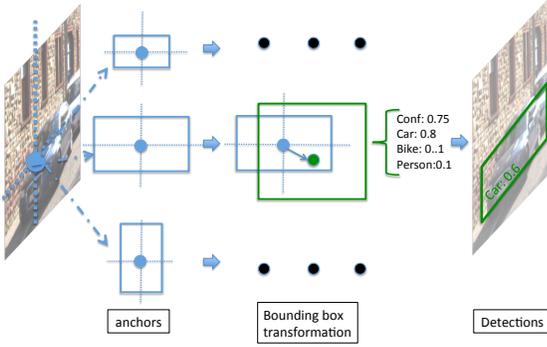


Figure 2. Bounding box transformation. Each grid center has K anchors with pre-selected shapes. Each anchor is transformed to its new position and shape using the relative coordinates computed by the *ConvDet* layer. Each anchor is associated with a confidence score and class probabilities to predict the category of the object within the bounding box.

whether an object exists and generating bounding box proposals for the object. The classification is handed over to fully connected layers, which are regarded as a “strong” classifier. But in fact, convolutional layers are “strong” enough to detect, localize, and classify objects at the same time.

For simplicity, we denote the detection layers of YOLO [21] as *FcDet* (only counting the last two fully connected layers). Compared with *FcDet*, the *ConvDet* layer has orders of magnitude fewer parameters and is still able to generate more region proposals with higher spatial resolution. The comparison between *ConvDet* and *FcDet* is illustrated in Fig. 3.

Assume that the input feature map is of size (W_f, H_f, Ch_f) , W_f is the width of the feature map, H_f is the height, and Ch_f is the number of input channels to the detection layer. Denote *ConvDet*’s filter width as F_w and height as F_h . With proper padding/striding strategy, the output of *ConvDet* keeps the same spatial dimension as the feature map. To compute $K \times (4 + 1 + C)$ outputs for each reference grid, the number of parameters required by the *ConvDet* layer is $F_w F_h Ch_f K (5 + C)$.

The *FcDet* layer described in [21] is comprised of two fully connected layers. Using the same notation for the input feature map and assuming the number of outputs of the *fc1* layer is F_{fc1} , then the number of parameters in the *fc1* layer is $W_f H_f Ch_f F_{fc1}$. The second fully connected layer in [21] generates C class probabilities as well as $K \times (4 + 1)$ bounding box coordinates and confidence scores for each of the $W_o \times H_o$ grids. Thus, the number of parameters in the *fc2* layer is $F_{fc1} W_o H_o (5K + C)$. The total number of parameters in these two fully connected layers is

	RP	cls	#Parameter
RPN	✓	✗	$Ch_f K (5 + C)$
<i>ConvDet</i>	✓	✓	$F_w F_h Ch_f K (5 + C)$
<i>FcDet</i>	✓	✓	$F_{fc1} (W_f H_f Ch_f + W_o H_o (5K + C))$

Table 1. Comparison between RPN, *ConvDet* and *FcDet*. RP stands for region proposition. cls stands for classification.

$$F_{fc1} (W_f H_f Ch_f + W_o H_o (5K + C)).$$

In [21], the input feature map is of size $7 \times 7 \times 1024$. $F_{fc1} = 4096$, $K = 2$, $C = 20$, $W_o = H_o = 7$, thus the total number of parameters required by the two fully connected layers is approximately 212×10^6 . If we keep the feature map sizes, number of output grid centers, classes, and anchors the same, and use 3×3 *ConvDet*, it would only require $3 \times 3 \times 1024 \times 2 \times 25 \approx 0.46 \times 10^6$ parameters, which is 460X smaller than *FcDet*. The comparison of RPN, *ConvDet* and *FcDet* is illustrated in Fig. 3 and summarized in Table 1.

3.3. Training protocol

Unlike Faster R-CNN [22], which deploys a (4-step) alternating training strategy to train RPN and detector network, our SqueezeDet detection network can be trained end-to-end, similarly to YOLO [21].

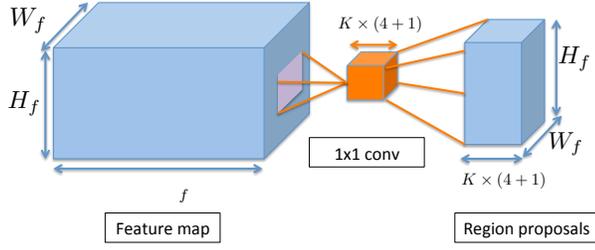
To train the *ConvDet* layer to learn detection, localization and classification, we define a multi-task loss function:

$$\begin{aligned}
& \frac{\lambda_{bbox}}{N_{obj}} \sum_{i=1}^W \sum_{j=1}^H \sum_{k=1}^K I_{ijk} [(\delta x_{ijk} - \delta x_{ijk}^G)^2 + (\delta y_{ijk} - \delta y_{ijk}^G)^2 \\
& \quad + (\delta w_{ijk} - \delta w_{ijk}^G)^2 + (\delta h_{ijk} - \delta h_{ijk}^G)^2] \\
& + \sum_{i=1}^W \sum_{j=1}^H \sum_{k=1}^K \frac{\lambda_{conf}^+}{N_{obj}} I_{ijk} (\gamma_{ijk} - \gamma_{ijk}^G)^2 + \frac{\lambda_{conf}^-}{WHK - N_{obj}} \bar{I}_{ijk} \gamma_{ijk}^2 \\
& \quad + \frac{1}{N_{obj}} \sum_{i=1}^W \sum_{j=1}^H \sum_{k=1}^K \sum_{c=1}^C I_{ijk} l_c^G \log(p_c).
\end{aligned} \tag{2}$$

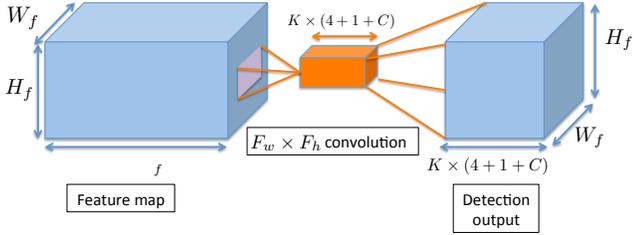
The first part of the loss function is the bounding box regression. $(\delta x_{ijk}, \delta y_{ijk}, \delta w_{ijk}, \delta h_{ijk})$ corresponds to the relative coordinates of anchor- k located at grid center- (i, j) . They are outputs of the *ConvDet* layer. The ground truth bounding box δ_{ijk}^G , or $(\delta x_{ijk}^G, \delta y_{ijk}^G, \delta w_{ijk}^G, \delta h_{ijk}^G)$, is computed as:

$$\begin{aligned}
\delta x_{ijk}^G &= (x^G - \hat{x}_i) / \hat{w}_k, \\
\delta y_{ijk}^G &= (y^G - \hat{y}_j) / \hat{h}_k, \\
\delta w_{ijk}^G &= \log(w^G / \hat{w}_k), \\
\delta h_{ijk}^G &= \log(h^G / \hat{h}_k).
\end{aligned} \tag{3}$$

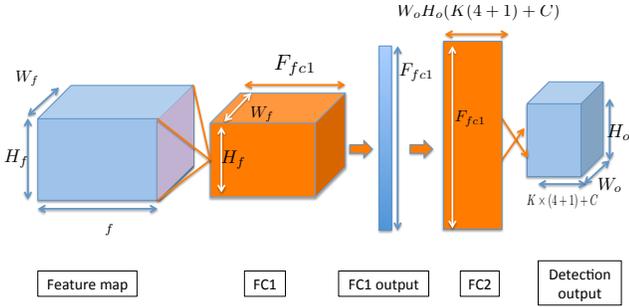
Note that Equation 3 is essentially the inverse transformation of Equation 1. (x^G, y^G, w^G, h^G) are coordinates of



(a) Last layer of Region Proposal Network (RPN) is a 1x1 convolution with $K \times (4 + 1)$ outputs. 4 is the number of relative coordinates, and 1 is the confidence score. It's only responsible for generating region proposals. The parameter size for this layer is $Ch_f \times K \times 5$.



(b) The *ConvDet* layer is a $F_w \times F_h$ convolution with output size of $K \times (5 + C)$. It's responsible for both computing bounding boxes and classifying the object within. The parameter size for this layer is $F_w F_h Ch_f K(5 + C)$.



(c) The detection layer of YOLO [21] contains 2 fully connected layers. The first one is of size $W_f H_f Ch_f F_{fc1}$. The second one is of size $F_{fc1} W_o H_o K(5 + C)$.

Figure 3. Comparing RPN, *ConvDet* and the detection layer of YOLO [21]. Activations are represented as blue cubes and layers (and their parameters) are represented as orange ones. Activation and parameter dimensions are also annotated.

a ground truth bounding box. During training, we compare ground truth bounding boxes with all anchors and assign them to the anchors that have the largest overlap (IOU) with each of them. The reason is that we want to select the “closest” anchor to match the ground truth box such that the transformation needed is reduced to minimum. I_{ijk} evaluates to 1 if the k -th anchor at position- (i, j) has the largest

overlap with a ground truth box, and to 0 if no ground truth is assigned to it. This way, we only include the loss generated by the “responsible” anchors. As there can be multiple objects per image, we normalize the loss by dividing it by the number of objects.

The second part of the loss function is confidence score regression. γ_{ijk} is the output from the *ConvDet* layer, representing the predicted confidence score for anchor- k at position- (i, j) . γ_{ijk}^G is obtained by computing the IOU of the predicted bounding box with the ground truth bounding box. Same as above, we only include the loss generated by the anchor box with the largest overlap with the ground truth. For anchors that are not “responsible” for the detection, we penalize their confidence scores with the $\bar{I}_{ijk} \gamma_{ijk}^2$ term, where $\bar{I}_{ijk} = 1 - I_{ijk}$. Usually, there are much more anchors that are not assigned to any object. In order to balance their influence, we use λ_{conf}^+ and λ_{conf}^- to adjust the weight of these two loss components. By definition, the confidence score’s range is $[0, 1]$. To guarantee that γ_{ijk} falls into that range, we feed the corresponding *ConvDet* output into a *sigmoid* function to normalize it.

The last part of the loss function is just cross-entropy loss for classification. $l_c^G \in \{0, 1\}$ is the ground truth label and $p_c \in [0, 1], c \in [1, C]$ is the probability distribution predicted by the neural net. We used *softmax* to normalize the corresponding *ConvDet* output to make sure that p_c is ranged between $[0, 1]$.

The hyper-parameters in Equation 2 are selected empirically. In our experiments, we set $\lambda_{bbox} = 5, \lambda_{conf}^+ = 75, \lambda_{conf}^- = 100$. This loss function can be optimized directly using back-propagation.

3.4. Neural Network Design

So far in this section, we described the single-stage detection pipeline, the *ConvDet* layer, and the end-to-end training protocol. These parts are universal and can work with various CNN architectures, including VGG16[24], ResNet[15], etc. When choosing the “backbone” CNN structure, our focus is mainly on model size and energy efficiency, and SqueezeNet[16] is our top candidate.

Model size. SqueezeNet is built upon *Fire Module*, which is comprised of a *squeeze* layer as input, and two parallel *expand* layers as output. The *squeeze* layer is a 1x1 convolutional layer that compresses an input tensor with large channel size to one with the same batch and spatial dimension, but smaller channel size. The *expand* layer is a mixture of 1x1 and 3x3 convolution filters that takes the compressed tensor as input, retrieve the rich features and output an activation tensor with large channel size. The alternating *squeeze* and *expand* layers effectively reduces parameter size without losing too much accuracy.

Energy efficiency. Different operations involved in neural network inference have varying energy needs. The most

expensive operation is DRAM access, which uses 100 times more energy than SRAM access and floating point operations [14]. Thus, we want to reduce DRAM access as much as possible.

The most straightforward strategy to reduce DRAM access is to use small models which reduces memory access for parameters. An effective way to reduce parameter size is to use convolutional layers instead of fully connected layers when possible. Convolution parameters can be accessed once and reused across all neighborhoods of all data items (if $\text{batch} > 1$) of the input data. However, the FC layer only exposes parameter reuse opportunities in the “batch” dimension, and each parameter is only used on one neighborhood of the input data. Besides model size, another important aspect is to control the size of intermediate activations. Assume the SRAM size of the computing hardware is 16MB, the SqueezeNet model size is 5MB. If the total size of activation output of any two consecutive layers is less than 11MB, then all the memory accesses can be completed in SRAM, no DRAM accesses are needed. A detailed energy efficiency discussion will be provided as supplementary material to this paper.

In this paper, we adopted two versions of the SqueezeNet architecture. The first one is the SqueezeNet v1.1 model⁵ with 4.72MB of model size and $> 80.3\%$ ImageNet top-5 accuracy. The second one is a more powerful SqueezeNet variation with squeeze ratio of 0.75, 86.0% of ImageNet accuracy and 19MB of model size [16]. In this paper, we denote the first model as SqueezeDet and the second one as SqueezeDet+. We pre-train these two models for ImageNet classification, then we add two fire modules with randomly initialized weight on top of the pretrained model, and connect to the *ConvDet* layer.

4. Experiments

We evaluated our model on the KITTI [9] object detection dataset, which is designed with autonomous driving in mind. We analyzed our model’s accuracy measured by average precision (AP), recall, speed and model size, and then compare with other top ranked methods on the KITTI leader board. Next, we analyzed the trade-off between accuracy and cost in terms of model size, FLOPS and activation size by tuning several key hyperparameters. We implemented our model’s training, evaluation, error analysis and visualization pipeline using Tensorflow [1], compiled with the cuDNN [4] computational kernels. The energy efficiency experiments of our model will be reported in the supplementary material.

4.1. KITTI object detection

Experimental setup. In our experiments, unless specified otherwise, we scaled all the input images to 1242x375.

⁵<https://github.com/DeepScale/SqueezeNet/>

We randomly split the 7381 training images in half into a training set and a validation set. Our average precision (AP) results are on the validation set. We used Stochastic Gradient Descent with momentum to optimize the loss function. We set the initial learning rate to 0.01, learning rate decay factor to 0.5 and decay step size to 10000. Instead of using a fixed number of steps, we trained our model all the way until the mean average precision (mAP)⁶ on the training set converges, and then evaluate the model on the validation set. Unless specifically specified, we used batch size of 20. We adopted data augmentation techniques such as random cropping and flipping to reduce overfitting. We trained our model to detect 3 categories of object, car, cyclist, pedestrian and used 9 anchors for each grid in our model. At the inference stage, we only kept the top 64 detections with highest confidence, and use NMS to filter the bounding boxes. We used NVIDIA TITAN X GPUs for our experiments.

Average Precision. The detection accuracy, measured by average precision is shown in Table 2. Our proposed SqueezeDet+ model achieved the best AP in all three difficulty levels of cyclist detection on the KITTI leader board. Its mean average precision of all 3 difficulty levels in 3 categories outperforms the top published methods [26, 3]. To evaluate whether *ConvDet* can be applied to other backbone CNN architectures, we appended *ConvDet* to the convolution layers of the VGG16 and ResNet50 models. In Table 2, observe that both of these models achieved competitive AP especially on car and cyclist detection. Example of error detections in different types are visualized in Fig. 4.

Recall. Recall is essential for the safety of autonomous vehicles, so we now analyze the recall of our proposed models. For each image with resolution of 1242x375, SqueezeDet generates in total 15048 bounding box predictions. It’s intractable to perform non-maximum suppression on this many bounding boxes because of the quadratic time complexity of NMS with respect to number of bounding boxes. Thus we only kept the top 64 predictions to feed into NMS. An interesting question to ask is, how does the number of bounding boxes kept affect recall? We tested this with the following experiment: First, we collect all the bounding box predictions and sort them by their confidence. Next, for each image, we choose the top N_{box} bounding box predictions, and sweep N_{box} from 8 to 15048. Then, we evaluate the overall recall for all difficulty levels of all categories. The Recall- N_{box} curve is plotted in Fig. 5. As we could see, for SqueezeDet and its strengthened model, the top 64 bounding boxes’ overall recall is already larger than 80%. If using all the bounding boxes, the SqueezeDet models can achieve 91% and 92% overall recall. Increasing the image size by 1.5X, the total number of bounding boxes increased

⁶Mean of average precision in 3 difficulty levels (easy, medium, hard) of 3 categories (car, cyclist, pedestrian).

method	car			cyclist			pedestrian			mAP	model size (MB)	speed (FPS)
	E	M	H	E	M	H	E	M	H			
SubCNN [26]	90.8	89.0	79.3	79.5	71.1	62.7	83.3	71.3	66.4	77.0	-	0.2
MS-CNN [3]	90.0	89.0	76.1	84.1	75.5	66.1	83.9	73.7	68.3	78.5	-	2.5
PNET*	81.8	83.6	74.2	74.3	58.6	51.7	77.2	64.7	60.4	69.6	-	10
Pie*	89.4	89.2	74.2	84.6	76.3	67.6	84.9	73.2	67.6	78.6	-	0.83
FRCN+VGG16 [2]	92.9	87.9	77.3	-	-	-	-	-	-	-	485	1.7
FRCN+Alex [2]	94.7	84.8	68.3	-	-	-	-	-	-	-	240	2.9
SqueezeDet (ours)	90.2	84.7	73.9	82.9	75.4	72.1	77.1	68.3	65.8	76.7	7.9	57.2
SqueezeDet+ (ours)	90.4	87.1	78.9	87.6	80.3	78.1	81.4	71.3	68.5	80.4	26.8	32.1
VGG16 + <i>ConvDet</i> (ours)	93.5	88.1	79.2	85.2	78.4	75.2	77.9	69.1	65.1	79.1	57.4	16.6
ResNet50 + <i>ConvDet</i> (ours)	92.9	87.9	79.4	85.0	78.5	76.6	67.3	61.6	55.6	76.1	35.1	22.5

Table 2. Summary of detection accuracy, model size and inference speed of different models on KITTI object detection challenge. * denotes that it is from an anonymous submissions, thus citation is not available.



Figure 4. Example of detection errors.

to 35, 190 and the maximum recall using all bounding boxes increases to 95%.

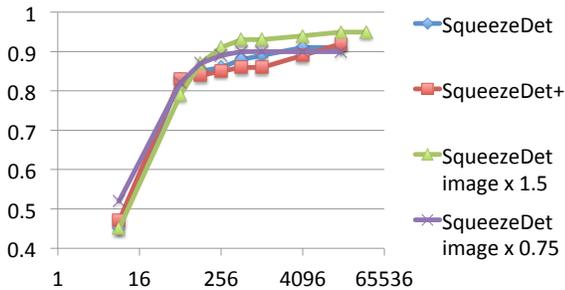


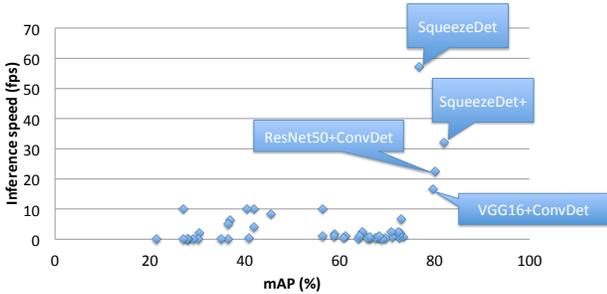
Figure 5. Overall recall vs N_{obj} for SqueezeDet and SqueezeDet+ models. We also tried to re-scale the input image by 1.5X and 0.75X. SqueezeDet and SqueezeDet+ model achieved the best recall of 0.91 and 0.92 with all bounding boxes. SqueezeDet with 1.5X image resolution achieved 0.95. SqueezeDet with 0.75X image resolution achieved 0.90.

Speed. Our models are the first to achieve real-time inference speed on KITTI dataset. To better understand the

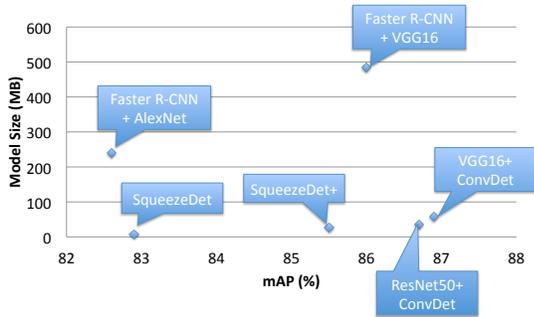
landscape, we collected statistics of 40 submissions of cyclist detection on the KITTI dataset, plotted their inference speed vs mean average precision of three difficulty levels of the cyclist category in Fig.6(a). At the time when this paper is written, the fastest model on the KITTI leader board is an anonymous submission named PNET with 10FPS of inference speed. Our proposed SqueezeDet model achieved 57.2 FPS with much better accuracy compared with PNET. With the stronger SqueezeDet+ model, we still obtained a speed of 32.1 FPS. With VGG16 and ResNet50 model, the inference speed is slightly slower, but still faster than all the existing KITTI submissions, as can be seen in Table 2 and Fig.6(a).

Model size. Since model size is not reported on the KITTI leader board, We compare our proposed models with Faster-RCNN based models from [2]. We plotted the model size and their mean average precision for 3 difficulty levels of the car category in Fig. 6(b) and summarized them in Table 2. As can be seen in Table 2, the SqueezeDet model is 61X smaller than the *Faster R-CNN + VGG16* model, and it is 30X smaller than the *Faster R-CNN + AlexNet* model.

In fact, almost 80% of the parameters of the VGG16 model are from the fully connected layers. Thus, after we replace the fully connected layers and RPN layer with *ConvDet*, the model size is only 57.4MB. Compared with YOLO [21] which is comprised of 24 convolutional layers, two fully connected layers with a model size of 753MB, SqueezeDet, without any compression, is 95X smaller.



(a) Inference speed vs mean average precision for cyclist detection. Each point represents one method’s speed-vs-accuracy tradeoff.



(b) Model size vs. mean average precision for car detection. Each point on this plane represents a method’s model size and accuracy tradeoff.

Figure 6. Comparison of different methods’ model size, inference speed, and accuracy on the KITTI dataset.

4.2. Design space exploration

We conducted design space exploration to evaluate some key hyper-parameters’ influence on our model’s overall detection accuracy (measured in mAP). Meanwhile, we also investigated the “cost” of these variations in terms of FLOPs, inference speed, model size and memory footprint. The results are summarized in Table 3, where the first row is our SqueezeDet architecture, subsequent rows are modifications to SqueezeDet, and the final row is SqueezeDet+.

Image resolution. For object detection, increasing image resolution is often an effective approach to improve detection accuracy [2]. But, larger images lead to larger activations, more FLOPs, longer training time, etc. We now evaluate some of these tradeoffs. In our experiments, we scaled the image resolution by 1.5X and 0.75X receptively.

DSE	mAP (%)	Speed (FPS)	FLOPs $\times 10^9$	Model Size (MB)	Activation Memory Footprint (MB)
SqueezeDet	76.7	57.2	9.7	7.9	117.0
scale-up	72.4	31.3	22.5	7.9	263.3
scale-down	73.2	92.5	5.3	7.9	65.8
16 anchors	66.9	51.4	11.0	9.4	117.4
SqueezeDet+	80.4	32.1	77.2	26.8	252.7

Table 3. Design space exploration for SqueezeDet. Different approaches with their accuracy, FLOPs per image, inference speed, model size and activation memory footprint. The speed, FLOPs and activation memory footprint are measured for batch size of 1. We used mean average precision (mAP) to evaluate the overall accuracy on the KITTI object detection task.

With larger input image, the training becomes much slower, so we reduced the batch size to 10. As we can see in Table 3, scaling up the input image actually decreases the mAP and also leads to more FLOPs, lower speed, and larger memory footprint. We also do an experiment with decreasing the image size. Scaling down the image leads to an astonishing 92.5 FPS of inference speed and a smaller memory footprint, though it suffers from a 3 percentage point drop in mean-average precision.

Number of anchors. Another hyper-parameter to tune is the number of anchors. Intuitively, the more anchors to use, the more bounding box proposals are to be generated, thus should result in a better accuracy. However, in our experiment in Table 3, using more anchors actually leads to lower accuracy. But, it also shows that for models that use *ConvDet*, increasing the number of anchors only modestly increases the model size, FLOPs, and memory footprint.

Model architecture. As we discussed before, by using a more powerful backbone model with more parameters significantly improved accuracy (See Table 3). But, this modification also costs substantially more in terms of FLOPs, model size and memory footprint.

5. Conclusion

We proposed SqueezeDet, a fully convolutional neural network for real-time object detection. We integrated the region proposition and classification into *ConvDet*, which is orders of magnitude smaller than its fully-connected counterpart. With the constraints of autonomous driving in mind, our proposed SqueezeDet and SqueezeDet+ models are designed to be small, fast, energy efficient, and accurate. On all of these metrics, our models advance the state-of-the-art.

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Supplementary Material: Designing Low Power Neural Network Architectures

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1. Low Power Neural Net Design Guideline

Different operations involved in the computation of a neural network consume different amounts of energy. According to [5], a DRAM access consumes two orders of magnitude more energy than a SRAM access or a floating point arithmetic operation. In this work, our main focus is on reducing memory accesses.

On-chip SRAM (Static Random Access Memory) and off-chip DRAM (Dynamic Random Access Memory) are the two major types of memory in computer hardware. Compared to off-chip DRAM, on-chip SRAM consumes about two orders of magnitude less energy, and SRAM read and write operations have lower latency and higher bandwidth than accessing off-chip DRAM. However, SRAM requires more transistors to store the same amount of data compared to DRAM. Thus modern processors typically have a large off-chip DRAM-based main memory and a small (*e.g.* 16MB) SRAM-based cache. During computation, processors prioritize SRAM for faster speed and lower energy consumption. But if the data size required for computation exceeds the on-chip SRAM capacity, processors will have to use off-chip DRAM.

The degree to which programmers can control the utilization of on-chip SRAM versus off-chip DRAM depends considerably on the hardware. For example, GPU programming typically involves manual management of SRAM-based register files and shared memory [3]. On the other hand modern CPU processors are the results of decades of architecture research aimed at simplifying programming in general, and memory access in particular. As a result the programmer can typically only generally encourage cache locality by the structure of data access in the program, leaving the processor hardware to improve data locality through cache protocols and pre-fetching. Thus, a simple and general rule to reduce energy consumed by memory accesses is to reduce the total memory footprint of the computations. In neural net computations this includes reducing the model parameters and intermediate layer activations. For hardware developers who aim to deploy the neural network on custom hardware (*e.g.* on an FPGA), more granular memory scheduling becomes possible. With a neural net model with

fewer model parameters and a perfect scheduling strategy, the hardware can cache all model parameters and activations of any two consecutive layers within on-chip SRAM, and no accesses to off-chip DRAM are necessary. This can lead to significant energy savings.

2. Memory Footprint

In what follows we analyze the memory footprint of SqueezeDet layer by layer. Details of the SqueezeDet model are shown in Table 1. The parameter size of SqueezeDet is just 7.9MB without compression, so it's possible for many processors to fit the entire model in on-chip SRAM and reuse the parameters in evaluations. The largest intermediate activation is the output of the *conv1* layer with 28.3MB. *conv1* is immediately followed by a max pooling layer. Potentially, we can fuse the implementation of max pooling and convolution layers such that the output of *conv1* can be immediately down-sampled by 4X and we only need to store about 7MB of activation to on-chip SRAM. Next, the *maxpool1* output is fed into *fire2*. The “squeeze” layer of the fire module compresses the input tensor and generates an activation with smaller channel size, and the two parallel “expand” layers of the fire module retrieve the compressed channel information and generate a larger output activation. The alternating “squeeze” and “expand” layers of the fire module effectively reduce the total size of activations of two consecutive layers. The following fire modules have increasingly larger channel size, but max pooling layers are used to reduce spatial resolution to control the activation size. Finally, even though the output of the final *ConvDet* layer encodes thousands of bounding box proposals, its activation size is negligible.

We counted the activation memory footprint for several models, including SqueezeDet, variations thereof, and others. Our results are summarized in Table 2. As we can see, SqueezeDet has a much lower memory footprint and performs fewer FLOPs compared to other models, leading to better energy efficiency for SqueezeDet.

model	Model		Activation		Inference Speed (FPS)	Energy Efficiency (J/frame)	mAP*
	Size (MB)	FLOPs $\times 10^9$	Memory Footprint (MB)	Average GPU Power (W)			
SqueezeDet	7.9	9.7	117.0	80.9	57.2	1.4	76.7
SqueezeDet: scale-up	7.9	22.5	263.3	89.9	31.3	2.9	72.4
SqueezeDet: scale-down	7.9	5.3	65.8	77.8	92.5	0.84	73.2
SqueezeDet: 16 anchors	9.4	11.0	117.4	82.9	51.4	1.6	66.9
SqueezeDet+	26.8	77.2	252.7	128.3	32.1	4.0	80.4
VGG16+ConvDet	57.4	288.4	540.4	153.9	16.6	9.3	79.1
ResNet50+ConvDet	35.1	61.3	369.0	95.4	22.5	4.2	76.1
Faster-RCNN + VGG16 [1]	485	-	-	200.1	1.7	117.7	-
Faster-RCNN + AlexNet [1]	240	-	-	143.1	2.9	49.3	-
YOLO**	753	-	-	187.3	25.8	7.3	-

Table 2. Comparing SqueezeDet and other models in terms of Energy efficiency and other aspects. The default image resolution is 1242x375, but the ‘‘SqueezeDet: scale-up’’ variation up-sampled input image’s height and width by 1.5X. The ‘‘scale-down’’ variation scaled image resolution by 0.75X. The default SqueezeDet model contains 9 anchors. But the 16-anchor variation contains 16 anchors for each grid. * The mAP denotes the mean average precision of 3 difficulty levels of 3 categories on KITTI dataset. It represents each model’s detection accuracy on KITTI dataset. ** We launched YOLO to detect 4, 952 VOC 2007 test images and it took 192 seconds to finish. We then compute the inference speed as $4, 952/192 \approx 25.8$ FPS, which is slower than the speed reported in [6]. The input image to YOLO is scaled to 448x448.

layer name/type	activation dimension	filter size/stride	$s_{1 \times 1}$	$e_{1 \times 1}$	$e_{3 \times 3}$	activation size (MB)	parameter size (MB)
Input	1242x375x3					5.3	
conv1	620x187x64	3x3/2 (x64)				28.3	0.007
maxpool1	309x93x64	3x3/2				7.0	
fire2	309x93x128		16	64	64	1.8 14.0	0.048
fire3	309x93x128		16	64	64	1.8 14.0	0.043
maxpool3	154x46x128	3x3/2				3.4	
fire4	154x46x256		32	128	128	0.86 6.9	0.17
fire5	154x46x256		32	128	128	0.86 6.9	0.19
maxpool5	76x22x256	3x3/2				1.6	
fire6	76x22x384		48	192	192	0.31 2.4	0.40
fire7	76x22x384		48	192	192	0.31 2.4	0.42
fire8	76x22x512		64	256	256	0.41 3.3	0.72
fire9	76x22x512		64	256	256	0.41 3.3	0.75
fire10	76x22x768		96	384	384	0.61 4.9	1.60
fire11	76x22x768		96	384	384	0.61 4.9	1.69
ConvDet	76x22x72	3x3/1 (x72)				0.46	1.90
						117.0 (total)	7.9 (total)

Table 1. Layer specification of SqueezeDet. $s_{1 \times 1}$ represents the number of 1x1 output filters in the *squeeze* layer, $e_{1 \times 1}$ is number of 1x1 filters in the *expand* layer and $e_{3 \times 3}$ is number of 3x3 filters in the *expand* layer.

3. Experiments

We measured the energy consumption of SqueezeDet and the other models during the object detection evaluation of 3741 images from the KITTI dataset [4]. The default input image resolution is 1242x375, and the batch size is set to 1. Meanwhile, we measured the GPU power usage with Nvidia’s system monitor interface (nvidia-smi). We sampled the power reading with a fixed interval of 0.1 second. Then, we obtained the power-vs-time curve as shown

in Fig 1. When the GPU is idle, it consumes about 15W of power. Through the evaluation process, the GPU went through several stages from idle to working and then to idle again. We denote the period with power measurement ≥ 20 W as working period. Then, we divide the working period evenly into 3 parts, and we take the measurements from the middle part to compute the average GPU power. The energy consumption per image is then computed as

$$\frac{\text{Average Power [Joule/Second]}}{\text{Inference Speed [Frame/Second]}}$$

We measured energy consumption of SqueezeDet and several other models using the above approach, and our experimental results are listed in Table 2. SqueezeDet consumes only 1.4J per image, which is $84\times$ less than the Faster R-CNN + VGG16 model. Scaling the image resolution down by $0.75\times$, the mAP drops by 3 percentage points, but the inference speed is $1.6\times$ faster and the energy consumption is less than 1J per image. With much better accuracy, SqueezeDet+ only consumes 4J per image, which is $>10\times$ more efficient than Faster R-CNN based methods. We combined the convolutional layers of VGG16 and ResNet50 with ConvDet, both models achieved much better energy efficiency compared with Faster R-CNN based models.

We also compared our models with YOLO. We use YOLO to detect 4, 952 images from the VOC 2007 [2] test set. The input images are scaled to 448x448, batch size is 1. It took YOLO 192 seconds to finish the evaluation. Using the same approach to measure the GPU power of YOLO, we compute the energy per frame of YOLO as 7.3J. Using the frame rate of 45FPS which is reported in [6], YOLO’s energy consumption per frame is 4.2J, which is comparable

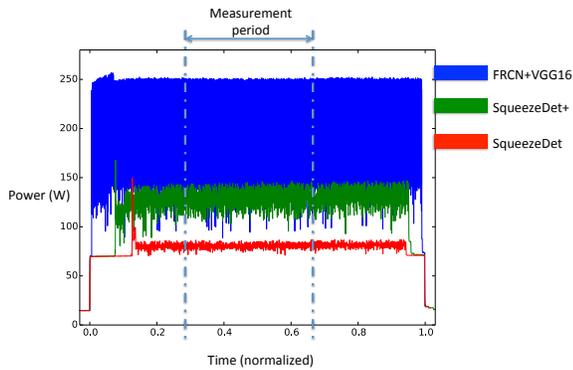


Figure 1. GPU power measured by `nvidia-smi`. Here we plot power measurement curve of 3 models, SqueezeDet, SqueezeDet+, and Faster R-CNN + VGG16 model. We normalize the working period of 3 models to the same range of $[0, 1]$. We divide the working period evenly into 3 parts and use the middle part to compute the average GPU power for each model.

with SqueezeDet+. But note that input image (with size of 1242×375) to SqueezeDet+ in our experiment contains 2X more pixels than the input image (448×448) to YOLO.

Our experiments show that SqueezeDet and its variations are very energy efficient compared with previous neural network based object detectors.

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